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12/04/01

# U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10000020	FILING DATE 12/04/2001	CLASS 438	SUBCLASS 1	GAU 281Z	EXAMINER S. H. ...
<b>**APPLICANTS:</b> Tago Masamoto; Tomita Yoshihiro;					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 366539-0000 12/04/2000					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO WNZ-2427	
TITLE : Method for laminating and mounting semiconductor chip					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-435L (Rev. 12-94)					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	
		Print Claim for O.G.	
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Primary Examiner	
		Application Examiner	
		<b>PREPARED FOR ISSUE</b>	
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